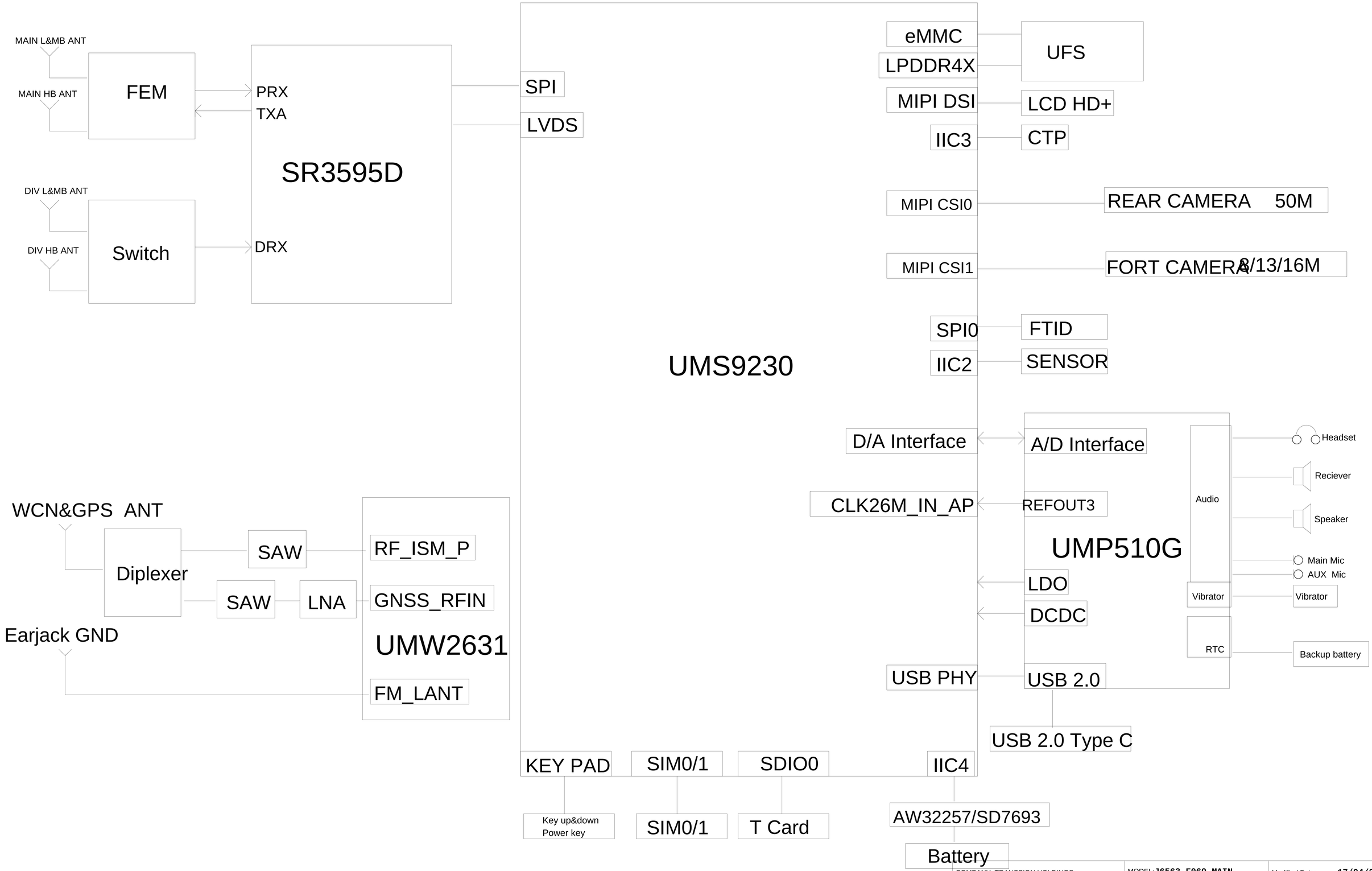
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

BLOCK DIAGRAM



COMPANY: TRANSSION HOLDINGS			MODEL: J6563_F069_MAIN	Modified Date:	17/04/2023:16:47
DRAWN		DATED	TITLE:		
CHECKED		DATED < >	Confidentiality CONFIDENTIAL	VERSION:	SHEET: 02 OF 23

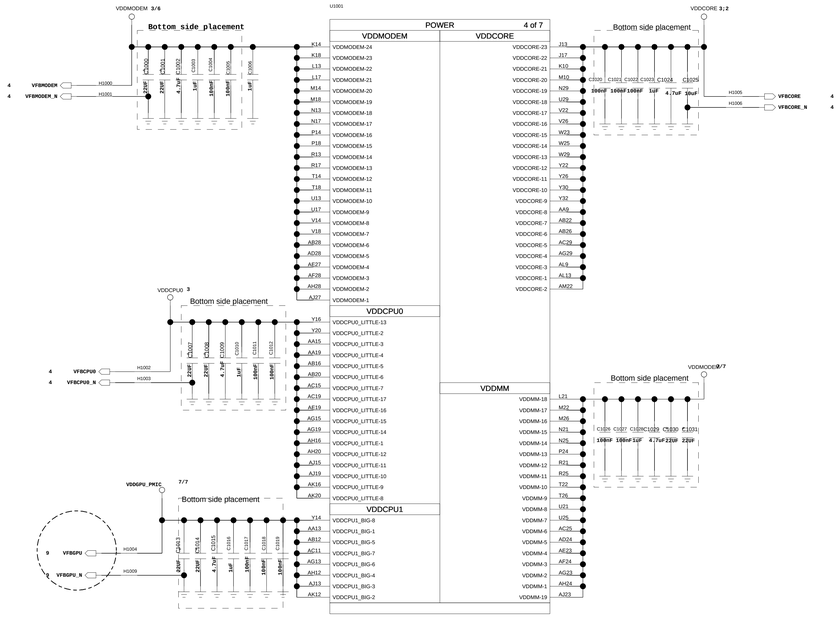
RF BLOCK DIAGRAM

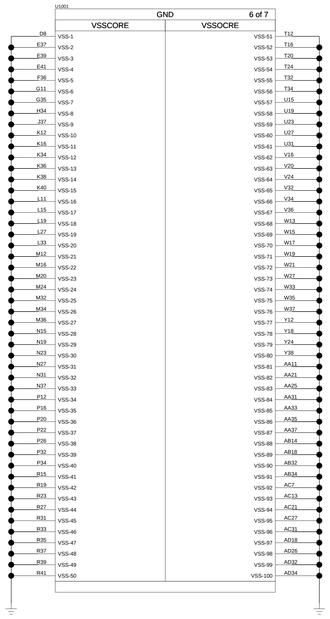
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

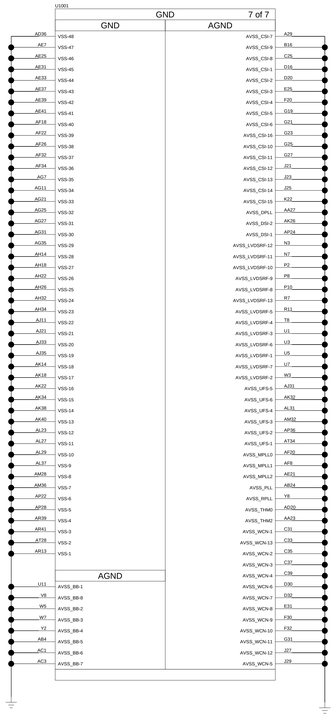
COMPANY: TRANSSION HOLDINGS				MODEL: **J6563_F069_MAIN**		Modified Date:	**17/04/2023:16:47**
DRAWN		DATED		TITLE:		VERSION:	SHEET: 1 OF 23
CHECKED		DATED	< >	Confidentiality	CONFIDENTIAL		

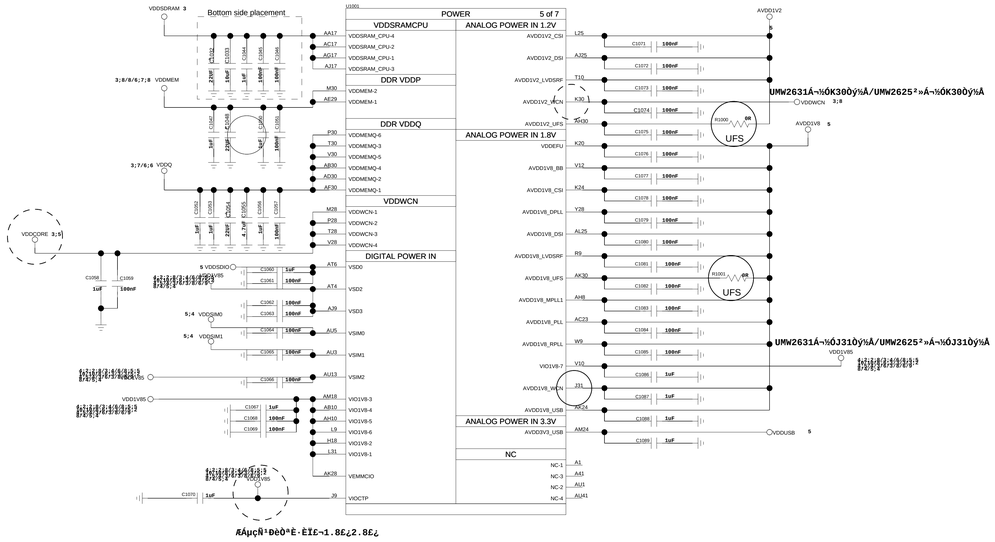
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

POWER





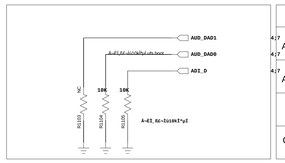




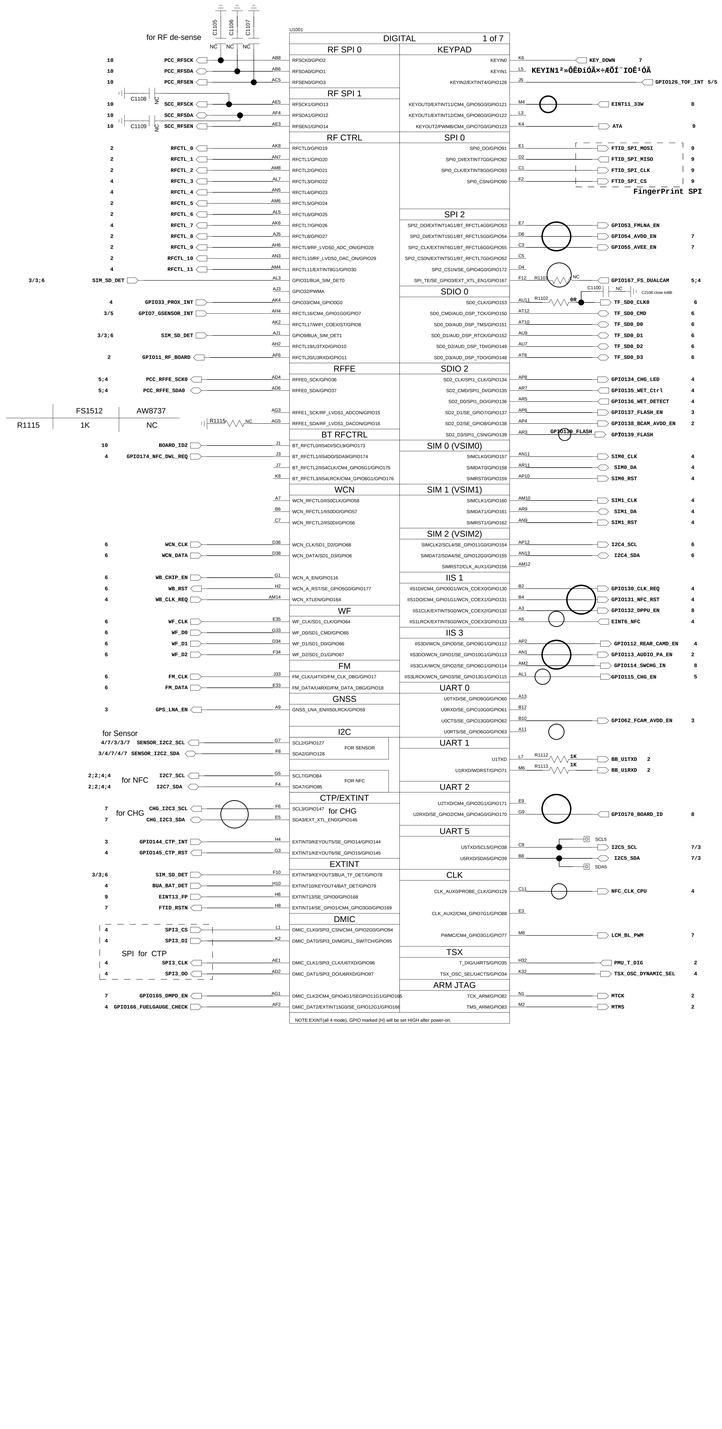
COMPANY: TRANSSION HOLDINGS			MODEL: J6563_F069_MAIN	Modified Date:	07/07/2023:16:40
DRAWN		DATED	TITLE:	VERSION:	SHEET: OF 23
CHECKED		DATED < >	Confidentiality CONFIDENTIAL		

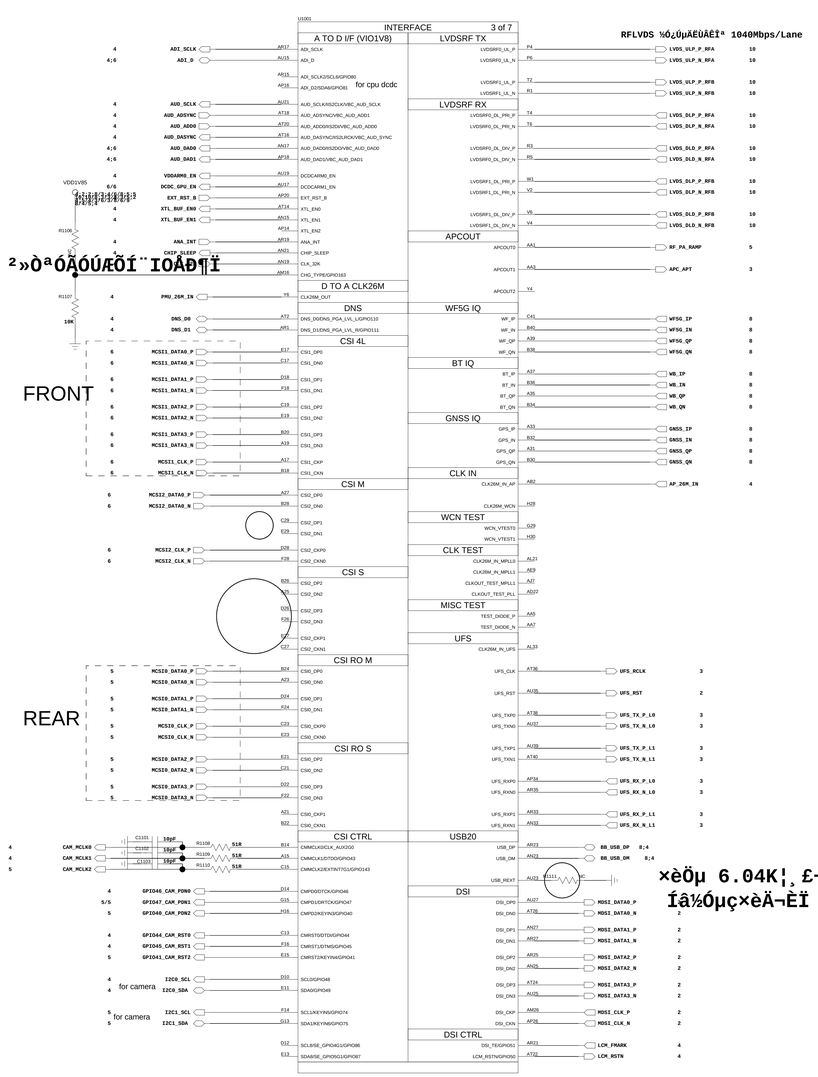
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

DIGITAL



Ball name	Signal name in boot code	Value	Descriptions	Default vale
AUD_DAD0	ARM_BOOT_MD1	00	Reserved	11(ie=1,wpu) (MSB=MD1, LSB=MD0)
		01	UFS boot	
AUD_DAD1	ARM_BOOT_MD0	10	SD boot	
		11	eMMC 6.5Mhz	
ADI_D	ARM_BOOT_MD3	0	NON-KEY Download(With timeout) mode	1(ie=1,wpu)
		1	Normal mode	
CHG_TYPE	ARM_BOOT_MD4	0	Other(DCP/other)	0(ie=1,wpd) Ìb³¨¼µÍÃÕØØ ºÍN6ProÓÐÊ¹±ÞÉ¬3s time out
		1	USB insert(SDP/CDP)	





COMPANY: TRANSSION HOLDINGS				MODEL: J6563_F069_MAIN	Modified Date: 07/07/2023:18:47
DRAWN		DATED		TITLE:	VERSION: SHEET: OF 23
CHECKED		DATED	< >	Confidentiality CONFIDENTIAL	

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

MEMORY

×«×ÓàÈMCPÉ¯umcpÉÓÉ±°ò DDRµÀÐQÍß°¯ÓÕµÚµÚÕò»ÀÐPINÀÕ°ÀÀ¬ÓÕ

×«×ÓàÈMMCÉ¯uf±ÉÓ·ÓÀèµÀÉ±°ò DDRµÀÐQÍß°¯ÓÕµÚ†µÀÐPINÀÕ°ÀÀ¬ÓÕ

U0301
MEMORY Order by Func.2 D.LPA(s)
MEMORY Order by Func.2 D.LPA(s)

				2 of 7			
			DATA LANE 0 A	CA A			
8	EMDQ0_A	F40	EMDQ4_A/EMDQ0_A	EMCK_T_A/EMCK_T_A	P36	EMCLK_A_P	8
8	EMDQ1_A	G41	EMDQ7_A/EMDQ1_A	EMCK_C_A/EMCK_C_A	N35	EMCLK_A_N	8
8	EMDQ2_A	H40	EMDQ5_A/EMDQ2_A				
8	EMDQ3_A	J41	EMDQ6_A/EMDQ3_A	EMCKE0_A/EMCKE0_A	M38	EMCKE0_A	8
8	EMDQ4_A	Z39	EMDM0_A/EMDQ4_A	EMCKE1_A/EMCKE1_A	L39	EMCKE1_A	8
8	EMDQ5_A	H38	EMDQ3_A/EMDQ5_A				
8	EMDQ6_A	G39	EMDQ2_A/EMDQ6_A	EMCS0_A/EMCS0_A	N39	EMCS0_A	8
8	EMDQ7_A	F38	EMDQ0_A/EMDQ7_A	EMCS1_A/EMCS1_A	P38	EMCS1_A	8
8	EMDMI0_A	G37	EMDQ1_A/EMDM0_A	EMCA1_A/EMCA0_A	N41	EMCA0_A	8
				EMCA0_A/EMCA1_A	P40	EMCA1_A	8
8	EMDQS0_A_P	H36	EMDQS0_T_A/EMDQS0_T_A	EMCA2_A/EMCA2_A	L35	EMCA2_A	8
8	EMDQS0_A_N	Z35	EMDQS0_C_A/EMDQS0_C_A	EMCA4_A/EMCA3_A	M40	EMCA3_A	8
			DATA LANE 1 A	EMCA3_A/EMCA4_A	L37	EMCA4_A	8
8	EMDQ8_A	V38	EMDQ10_A/EMDQ8_A	EMCA5_A/EMCA5_A	L41	EMCA5_A	8
8	EMDQ9_A	U39	EMDM1_A/EMDQ9_A	CA B			
8	EMDQ10_A	T38	EMDQ13_A/EMDQ10_A	EMCK_T_B/EMCK_T_B	AE35	EMCLK_B_P	8
8	EMDQ11_A	U37	EMDQ11_A/EMDQ11_A	EMCK_C_B/EMCK_C_B	AF36	EMCLK_B_N	8
8	EMDQ12_A	W41	EMDQ9_A/EMDQ12_A				
8	EMDQ13_A	V40	EMDQ12_A/EMDQ13_A	EMCKE0_B/EMCKE0_B	AH38	EMCKE0_B	8
8	EMDQ14_A	U41	EMDQ14_A/EMDQ14_A	EMCKE1_B/EMCKE1_B	AJ39	EMCKE1_B	8
8	EMDQ15_A	T40	EMDQ15_A/EMDQ15_A				
				EMCS0_B/EMCS0_B	AG39	EMCS0_B	8
8	EMDMI1_A	W39	EMDQ8_A/EMDM1_A	EMCS1_B/EMCS1_B	AF38	EMCS1_B	8
8	EMDQS1_A_P	T36	EMDQS1_T_A/EMDQS1_T_A	EMCA1_B/EMCA0_B	AG41	EMCA0_B	8
8	EMDQS1_A_N	U35	EMDQS1_C_A/EMDQS1_C_A	EMCA0_B/EMCA1_B	AF40	EMCA1_B	8
			DATA LANE 0 B	EMCA2_B/EMCA2_B	AG37	EMCA2_B	8
8	EMDQ0_B	AP40	EMDQ4_B/EMDQ0_B	EMCA4_B/EMCA3_B	AH40	EMCA3_B	8
8	EMDQ1_B	AN41	EMDQ7_B/EMDQ1_B	EMCA3_B/EMCA4_B	AJ37	EMCA4_B	8
8	EMDQ2_B	AM40	EMDQ5_B/EMDQ2_B	EMCA5_B/EMCA5_B	AJ41	EMCA5_B	8
8	EMDQ3_B	AL41	EMDQ6_B/EMDQ3_B				
8	EMDQ4_B	AL39	EMDM0_B/EMDQ4_B				
8	EMDQ5_B	AN38	EMDQ3_B/EMDQ5_B				
8	EMDQ6_B	AN39	EMDQ2_B/EMDQ6_B				
8	EMDQ7_B	AP38	EMDQ0_B/EMDQ7_B				
8	EMDMI0_B	AN37	EMDQ1_B/EMDM0_B				
8	EMDQS0_B_P	AL35	EMDQS0_T_B/EMDQS0_T_B				
8	EMDQS0_B_N	AK36	EMDQS0_C_B/EMDQS0_C_B				
			DATA LANE 1 B	EMMC			
8	EMDQ8_B	AR38	EMDQ10_B/EMDQ8_B	EMMC_CLK	AT30		
8	EMDQ9_B	AC39	EMDM1_B/EMDQ9_B	EMMC_CMD	AN31		
8	EMDQ10_B	AC37	EMDQ11_B/EMDQ10_B	EMMC_RST	AP32		
8	EMDQ11_B	AD38	EMDQ13_B/EMDQ11_B	EMMC_DS/GPIO105	AU31		
8	EMDQ12_B	AA41	EMDQ9_B/EMDQ12_B				
8	EMDQ13_B	AB40	EMDQ12_B/EMDQ13_B	EMMC_D0/BT_RFCTL0G1/GPIO98	AR31		
8	EMDQ14_B	AC41	EMDQ14_B/EMDQ14_B	EMMC_D1/BT_RFCTL1G1/GPIO108	AR29		
8	EMDQ15_B	AD40	EMDQ15_B/EMDQ15_B	EMMC_D2/BT_RFCTL2G1/GPIO109	AU33		
				EMMC_D3/BT_RFCTL3G1/GPIO106	AM30		
8	EMDMI1_B	AA39	EMDQ8_B/EMDM1_B	EMMC_D4/BT_RFCTL4G1/GPIO104	AP30		
				EMMC_D5/BT_RFCTL5G1/GPIO103	AT32		
8	EMDQS1_B_P	AB36	EMDQS1_T_B/EMDQS1_T_B	EMMC_D6/BT_RFCTL6G1/GPIO100	AU29		
8	EMDQS1_B_N	AC35	EMDQS1_C_B/EMDQS1_C_B UMS9230	EMMC_D7/BT_RFCTL7G1/GPIO101	AN29		

NOTE: using alternate to change order

EMRESET_N	Y40	EMRSTN	8		
EMZQ	D41	R1200 NC / R1201 240R	3;7/6;3	GND	

COMPANY: TRANSSION HOLDINGS				MODEL: J6563_F069_MAIN		Modified Date:	07/07/2023:16:40
DRAWN		DATED		TITLE:		VERSION:	SHEET: OF 23
CHECKED		DATED	< >	Confidentiality	CONFIDENTIAL		

PMU_POWER

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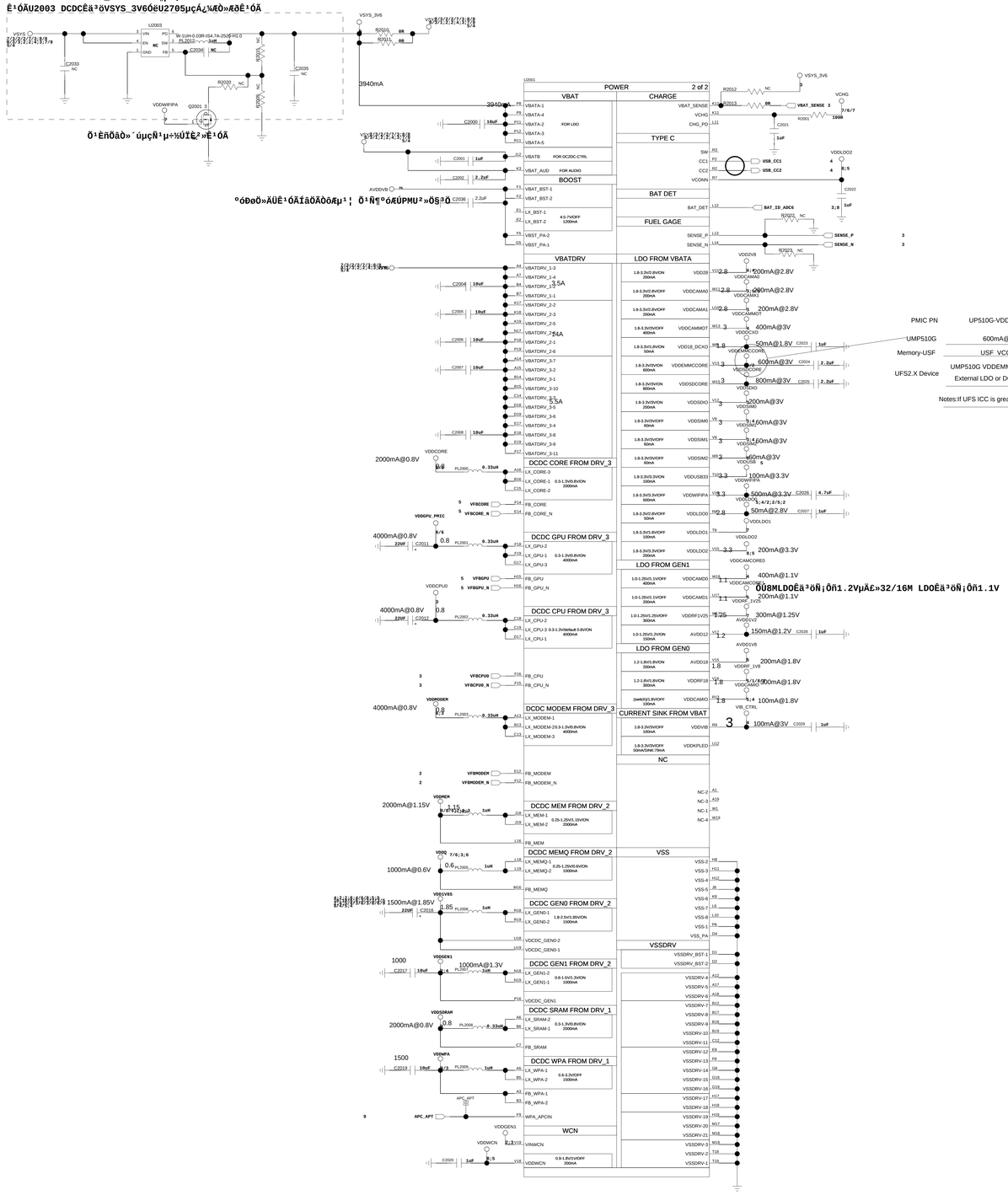
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

PL2012ÌùÃ¬Ê¹ÓÃ15103901/15102829
U2003 DCDCÊä³öVSYS_3V6ôëVSYS¶þÑ¡Ò»SMT
Ê¹ÓÃU2003 DCDCÊä³öVSYS_3V6ôëU2705µçÁ¿¼æÈ¡Ê¹ÓÃ



UMP510G DCDC output

Input Power	Power Name	Output Voltage (V)	Output Current (mA)	Default Voltage (V)	Default
VBAT	DCDC CORE	0.3-1.3	2000	0.8	ON
	DCDC GPU	0.3-1.3	4000	0.8	ON
	DCDC CPU	0.3-1.3	4000	0.8	ON
	DCDC MODEM	0.3-1.3	4000	0.8	ON
	DCDC MEM	0.25-1.25	2000	1.15	ON
	DCDC MEMQ	0.25-1.25	1000	0.6	ON
	DCDC GEN0	1.8-2.5	1500	1.85	ON
	DCDC GEN1	0.6-1.5	1000	1.3	ON
	DCDC SRAM	0.3-1.3	2000	0.8	ON
	DCDC WPA	0.6-3.3	1500	/	OFF

UMP510G LDO output

Input Power	Power Name	Output Voltage (V)	Output Current (mA)	Default Voltage (V)	Default
VBAT	VDD28	1.8-3.3	200	2.8	ON
	VDDCAMA0	1.8-3.3	200	2.8	OFF
	VDDCAMA1	1.8-3.3	200	2.8	OFF
	VDDCAMMOT	1.8-3.3	400	3	OFF
	VDD18_DCXO	1.8-3.3	50	1.8	ON
	VDDEMMCCORE	1.8-3.3	600	3	ON
	VDDSDCORE	1.8-3.3	800	3	ON
	VDDSDIO	1.8-3.3	200	3	ON
	VDDSIM0	1.8-3.3	60	3	OFF
	VDDSIM1	1.8-3.3	60	3	OFF
	VDDSIM2	1.8-3.3	60	3	OFF
	VDDUSB33	1.8-3.3	100	3.3	ON
	VDDWIFIPA	1.8-3.3	500	3.3	OFF
	VDDLDO0	1.8-3.3	50	2.8	OFF
	VDDLDO1	1.8-3.3	100	3.3	OFF
	VDDLDO2	1.8-3.3	200	3.3	OFF
GEN1	VDDCAMD0	1.0-1.25	400	1.1	OFF
	VDDCAMD1	1.0-1.25	200	1.1	OFF
	VDDRF1V25	1.0-1.25	300	1.25	OFF
	AVDD12	1.0-1.25	150	1.2	ON
GEN0	AVDD18	1.2-1.8	200	1.8	ON
	VDDRF18	1.2-1.8	300	1.8	ON
	VDDCAMIO	1.2-1.8	100	1.8	OFF
GEN1	WCN	1.0-1.6	200	1	OFF

COMPANY: TRANSSION HOLDINGS			MODEL:J6563_F069_MAIN	Modified Date:	08/07/2023:21:52
DRAWN		DATED		TITLE:	VERSION: / SHEET: 23
CHECKED		DATED	< >	Confidentiality: CONFIDENTIAL	

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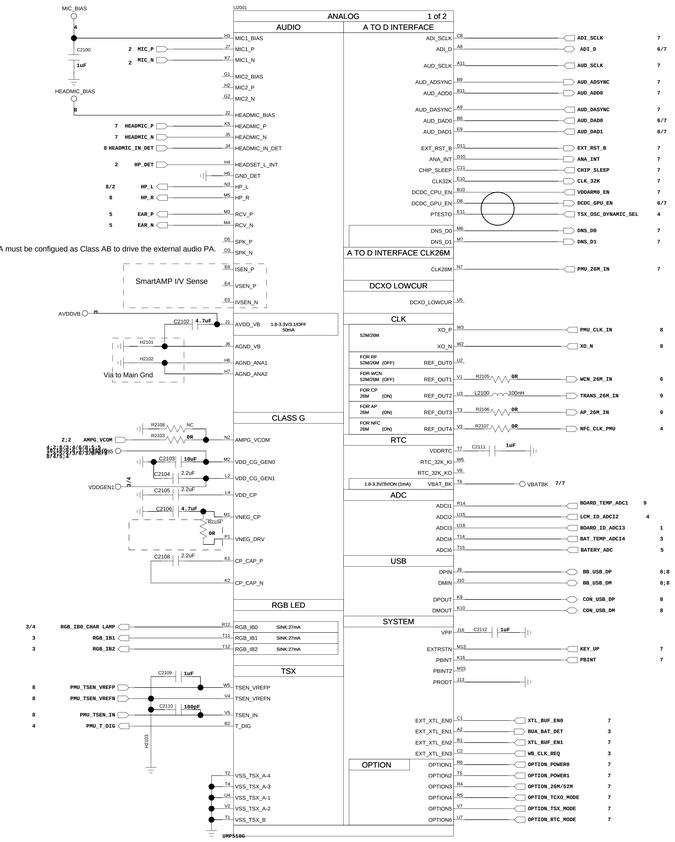
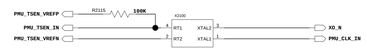
LTR	ECO NO:	APPROVED:	DATE:

PMU_ANALOG



BKBT

TSX



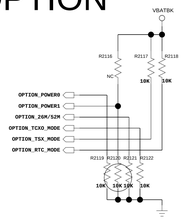
IDistance from TCXO/TSX to heat source should be 3mm/46mm separately at least..

Minimum 0.25mm-clearance route out area around TCXO/TSX to avoid heat conductivity effects..

Recommend no-copper area under TCXO/TSX from Layer L1 to Layer L(n-1)

Keep ¡Y1mm away from the PCB edge, don¡¯t refer to the PCB Main GND, refer PCB design guide for detail

OPTION



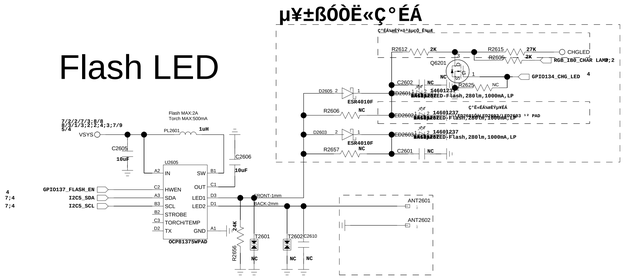
	Item	Scenario	Option1	Option2
-->	0	Qogir L6+UMP510G	0	0
	1	Qogir L6+UMP510G+DCDC	0	1
	2		1	0
	3		1	1

	Scenario	Option3(26M/52M)	Option4(TCXO_MODE)	Option5(TSX_MODE)	Option6(RTC_MODE)
	26M TCXO+32K Crystal	0	1	0	0
	26M TSX+32K Crystal	0	0	1	0
-->	26M TSX+32K-less from PMIC internal	0	0	1	1
	26M DCXO+32K-less from PMIC internal	0	1	1	1
	26M DCXO+32K Crystal	0	1	1	0

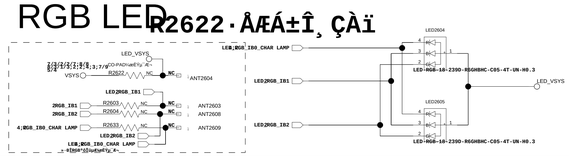
COMPANY: TRANSSION HOLDINGS			MODEL: J6563_F069_MAIN	Modified Date:	08/07/2023:21:46
DRAWN		DATED	TITLE:		
CHECKED		DATED < >	Confidentiality CONFIDENTIAL	VERSION:	SHEET: 0 OF 23

REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:

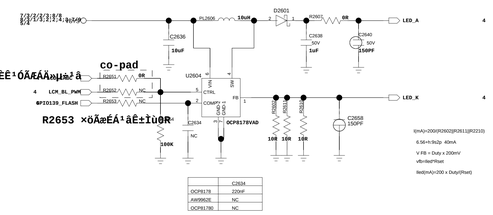
Flash LED



RGB LED



Back Light Driver
1uF£¬50V£¬0603



	C2634
DCP8178	220nF
AW99628	NC
DCP81780	NC

I(mA)=200/(R2602||R2611||R2210)

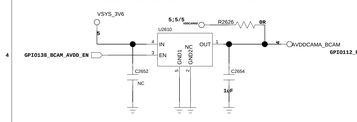
6.56+0.0b2p 40mA

V FB = Duty x 200mV

vfb=Iled*Rset

Iled(mA)=200 x Duty/(Rset)

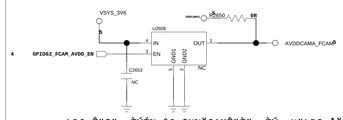
LDO BCAM AVDD



REAR VCAMD_EXT2(50M(0.7um))

¸ÃLDOÕ»ÊÇºóÉã50M(0.7um)Ê±ºòÊ¹ÓÃ,50M(0.6um)/13M/8MÊ¹ÓÃÆ½Ì¨µÄµçÓ´£¬LDOºÜ¹ó

LDO FCAM AVDD

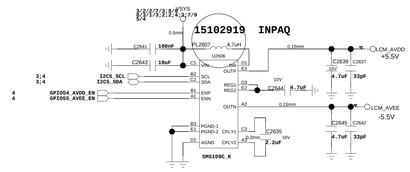


LDO FCAM DVDD(32M£©

LDO Êä³öµçÈÝÉ¼µõ£¬²¼¾ÕCAMÊäÈëµçÈÝ¿¿½üLDO·ÂÕÃ

LDO Êä³öµçÈÝÉ¼µõ£¬²¼¾ÕCAMÊäÈëµçÈÝ¿¿½üLDO·ÂÕÃ£»

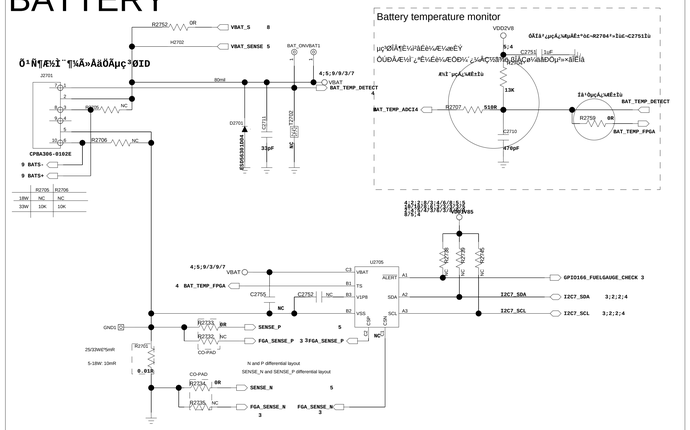
BIAS OCP2131:I2C Address= write:0x7C read:0x7D



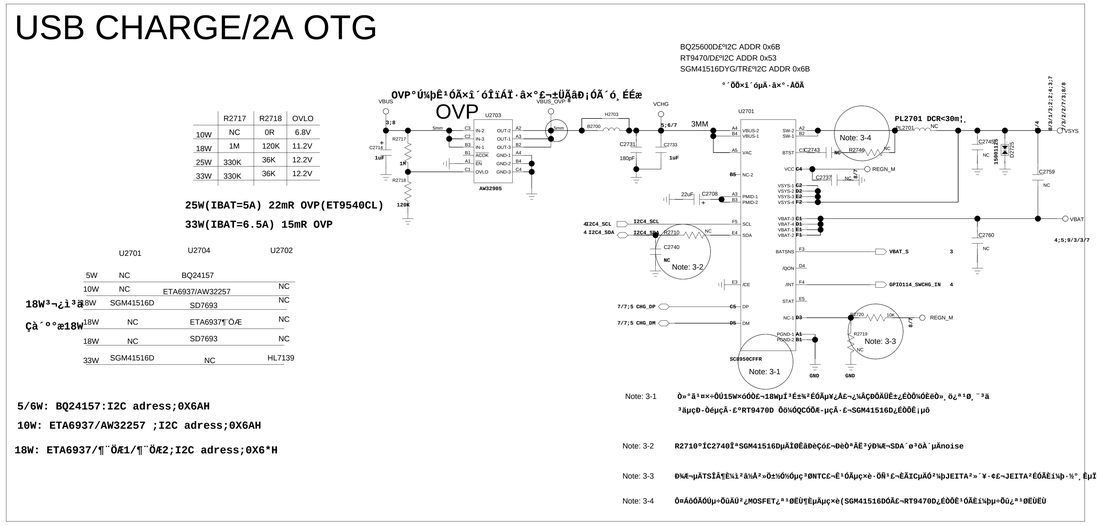
COMPANY: TRANSSION HOLDINGS				MODEL: J6563_F069_MAIN		Modified Date:	10/07/2023:11:22
DRAWN		DATED		TITLE:		VERSION:	SHEET: OF 23
CHECKED		DATED	< >	Confidentiality	CONFIDENTIAL		

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

BATTERY



USB CHARGE/2A OTG



BQ25600DE³2C ADDR 0x6B
RT9470/DE³2C ADDR 0x53
SGM41516DYG/TRE³2C ADDR 0x6B

	R2717	R2718	OVLO
10W	NC	0R	6.8V
18W	1M	120K	11.2V
25W	330K	36K	12.2V
33W	330K	36K	12.2V

25W(IBAT=5A) 22mR OVP(ET9540CL)

33W(IBAT=6.5A) 15mR OVP

	U2701	U2704	U2702
5W	NC	BQ24157	
10W	NC	ETA6937/AW32257	NC
18W	SGM41516D	SD7693	NC
18W	NC	ETA6937¶¨ÓÆ	NC
18W	NC	SD7693	NC
33W	SGM41516D	NC	HL7139

18W³¬¿ì³ä

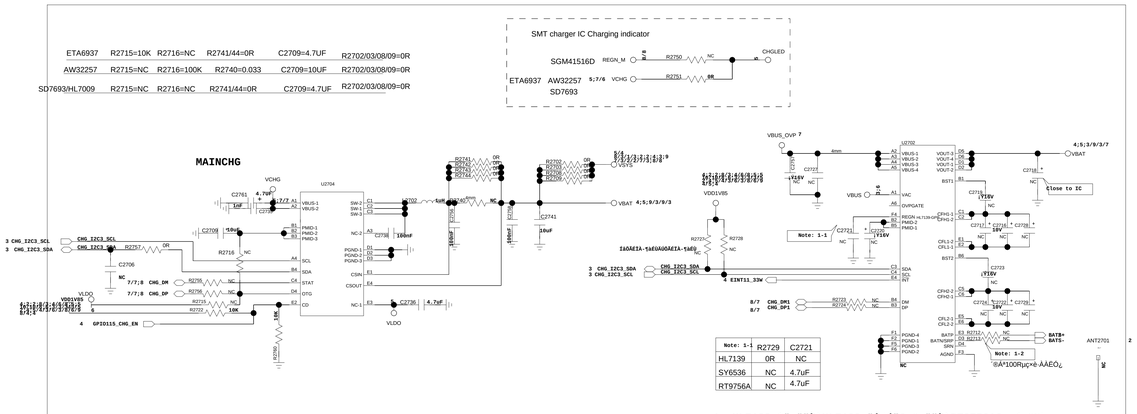
Çà´°°æ18W

5/6W: BQ24157:I2C adress;0X6AH

10W: ETA6937/AW32257 ;I2C adress;0X6AH

18W: ETA6937/¶¨ÓÆ1/¶¨ÓÆ2;I2C adress;0X6*H

ETA6937	R2715=10K	R2716=NC	R2741/44=0R	C2709=4.7UF	R2702/03/08/09=0R
AW32257	R2715=NC	R2716=100K	R2740=0.033	C2709=10UF	R2702/03/08/09=0R
SD7693/HL7009	R2715=NC	R2716=NC	R2741/44=0R	C2709=4.7UF	R2702/03/08/09=0R



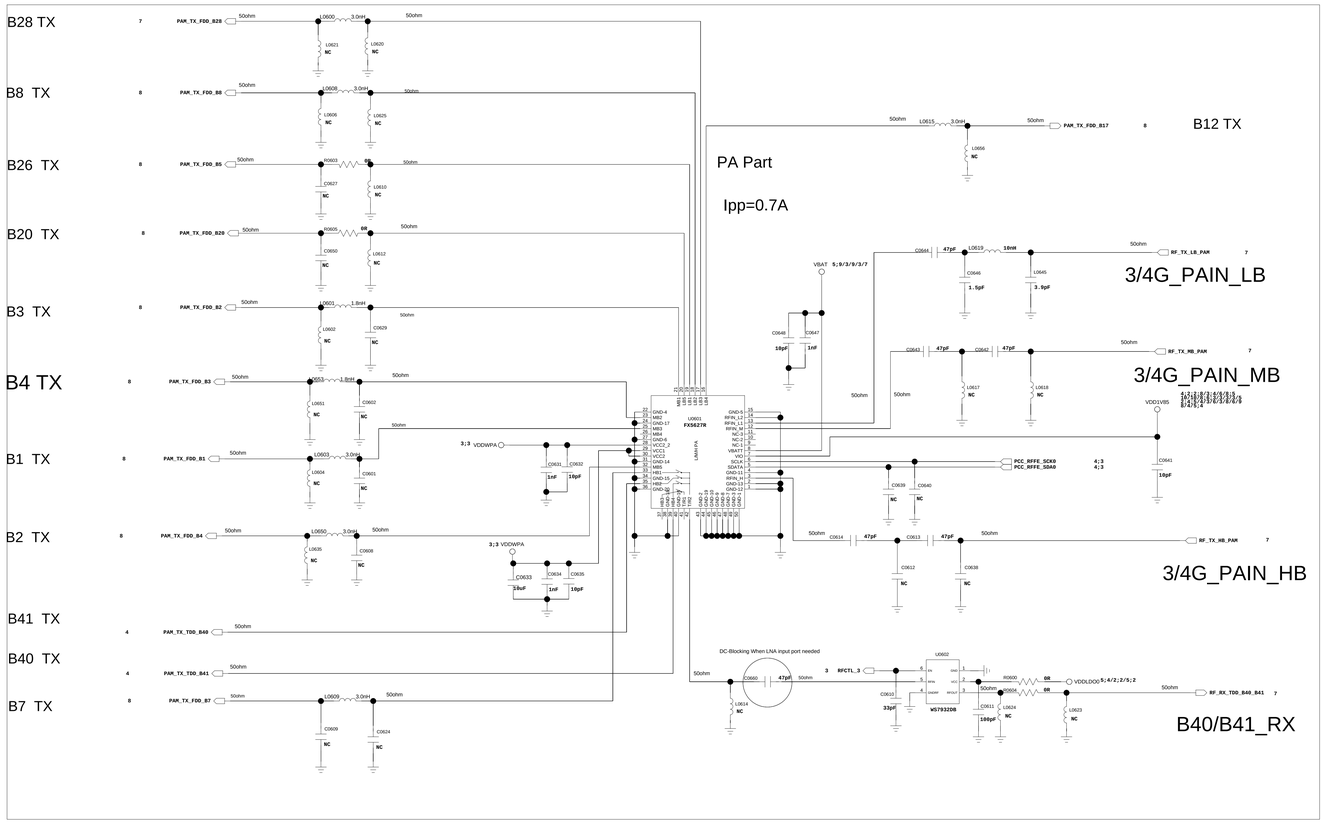
Note: 1-1	R2729	C2721
HL7139	0R	NC
SY6536	NC	4.7uF
RT9756A	NC	4.7uF

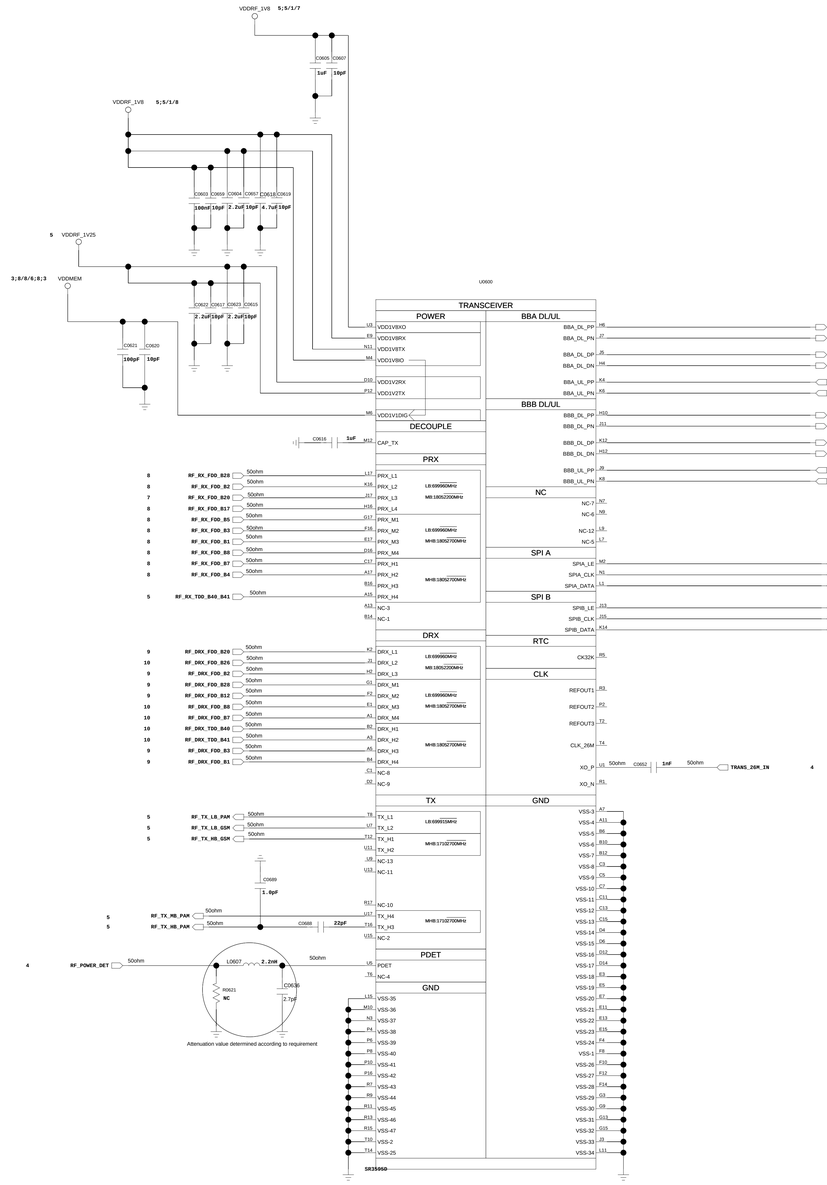
1. HL7139¡áÕ»ãÜÏòHL7139µäÏÿÁÍ6¬²¬ÃÜÏòRT9756A£¬»áx²¼þ
3¡¢33WE±²0GNbpÃ>bA-bEo*1ûµ ¥¬ANT2701E-1ão¡Ï»ÕÃbeO*×óÃ0µñ;

COMPANY: TRANSSION HOLDINGS		MODEL: J6563_F069_MAIN	Modified Date: 08/07/2023:22:10
DRAWN	DATED	TITLE:	
CHECKED	DATED < >	Confidentiality CONFIDENTIAL	VERSION: SHEET: OF 23

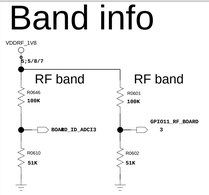
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

LTE/WCDMA PAM



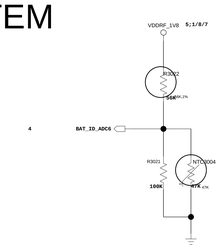


Band info



Market	Value	Band
Open India SEA	56k	G:B3/B8 W:B1/5/8 L:B1/3/5/8/40/41
¸ÇÖÞ	39k	G:B3/8 W:B1/8 L:B1/3/7/20
ÃÍ Ç÷È¹ÉØ÷ÖÐ¶Ø¶÷ ÆÃÃÉ±Ö	24k	G:B3/8 W:B1/8 L:B1/3/7/8/20/28/40/41
ÃÍÃÃ÷Ã-ÃÃ	10K	G:B2/5 W:B2/5 L:B2/4/5/7/28A/28B/66

PA TEM



COMPANY: TRANSSION HOLDINGS			MODEL: J6563_F069_MAIN	Modified Date:	07/07/2023:16:40
DRAWN		DATED	TITLE:	VERSION:	SHEET: OF 23
CHECKED		DATED < >	Confidentiality CONFIDENTIAL		

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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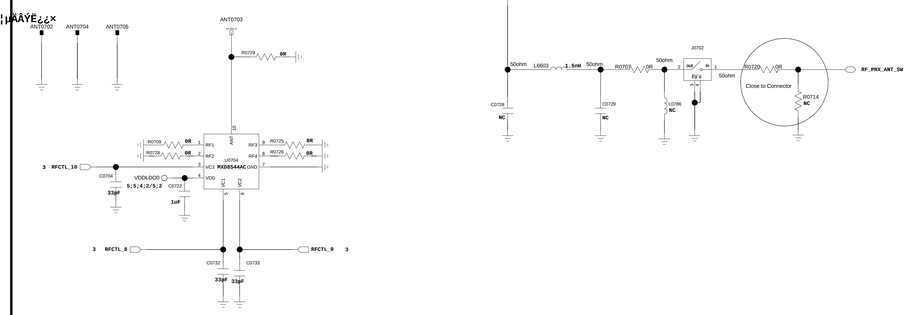
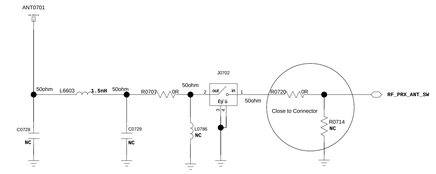
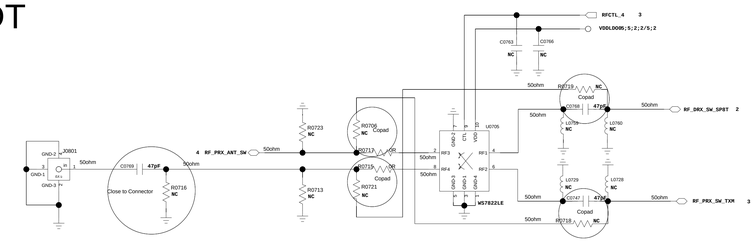
Primary Antenna

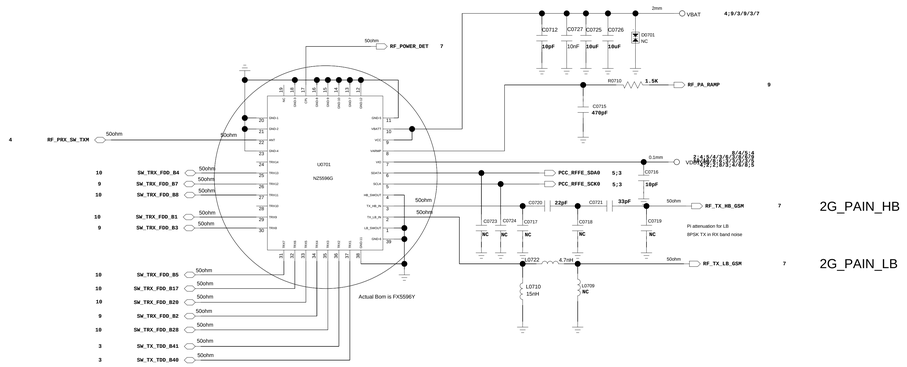




DPDT

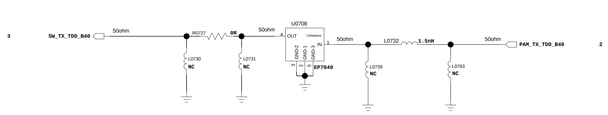


TDD B34/B39/GSM PA + Switch

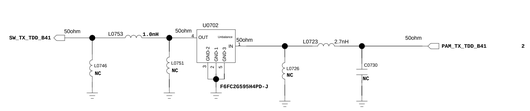


TDD LTE Parts

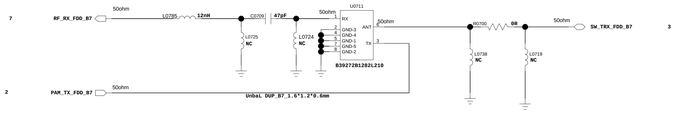
TDD_B40 TRX



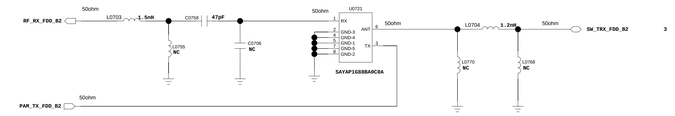
TDD_B41 TRX



TRX _B7



TRX _B2



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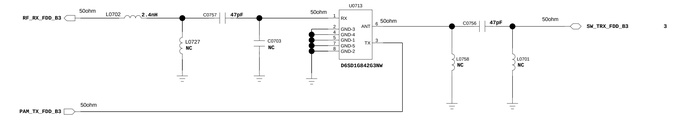
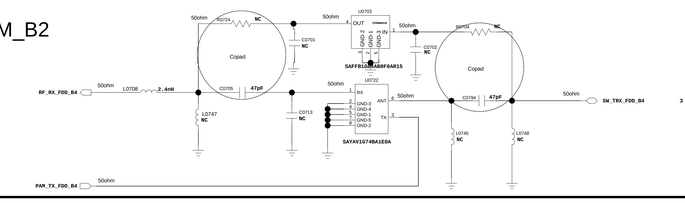
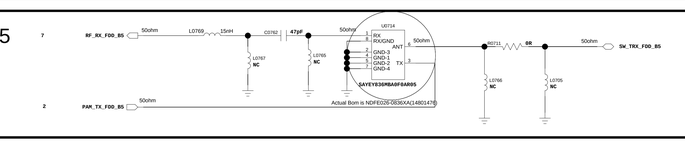


TRX_B4/GSM_B2



TRX_B26&B5



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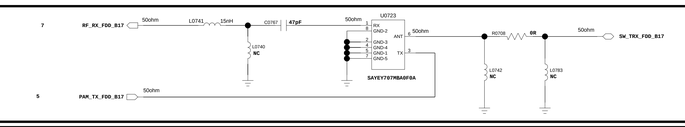

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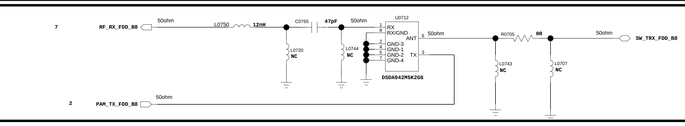

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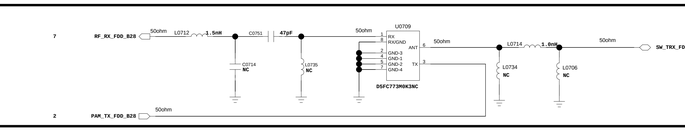

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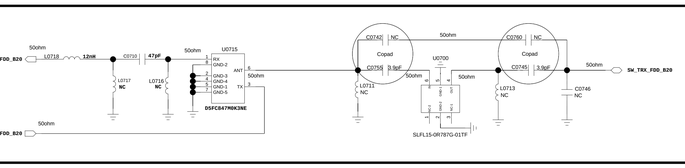
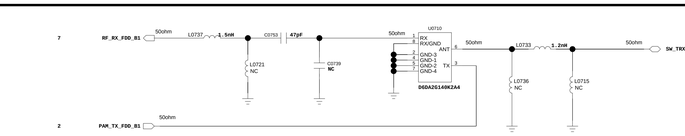


TRX_B1

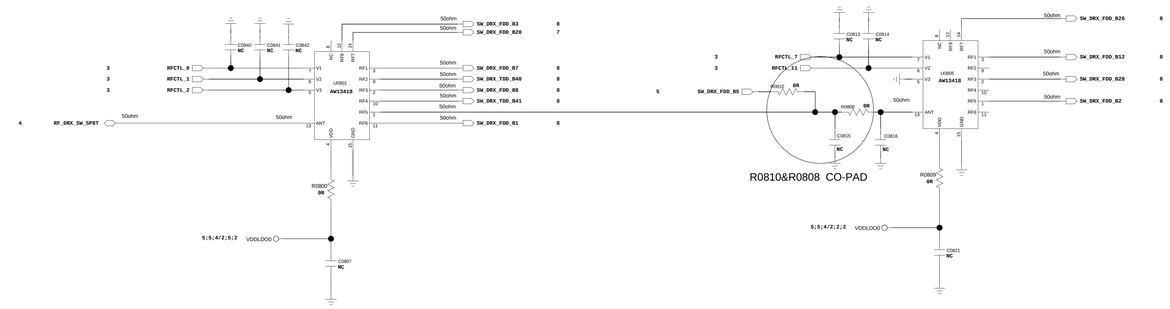


COMPANY: TRANSSION HOLDINGS		MODEL: J6563_F069_MAIN		Modified Date:	07/07/2023:16:40
DRAWN	DATED	TITLE:		VERSION:	SHEET: 0 of 23
CHECKED	DATED < >	Confidentiality	CONFIDENTIAL		

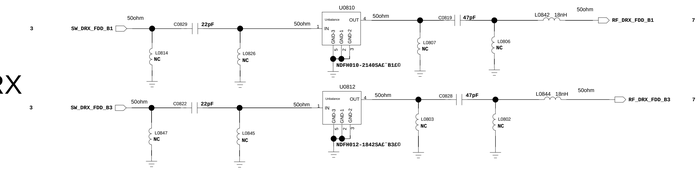
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

RF_SR3595D_RF_DRX_Path

RF_SR3595D_RF_DRX_SP8T



B1&B3_DRX



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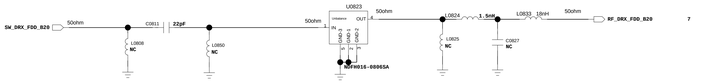

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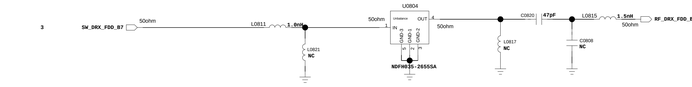

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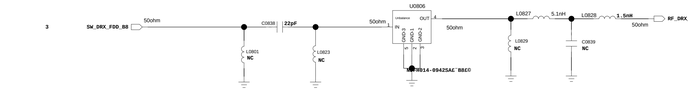

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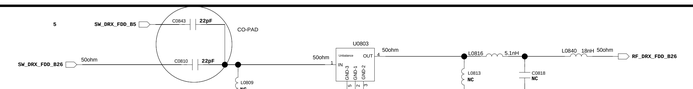
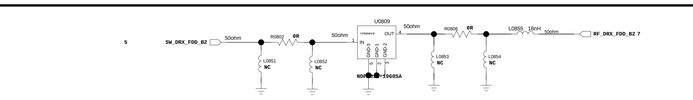


B2_DRX



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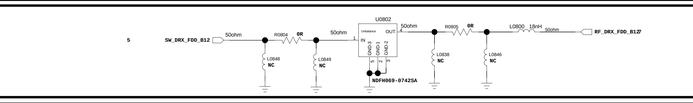

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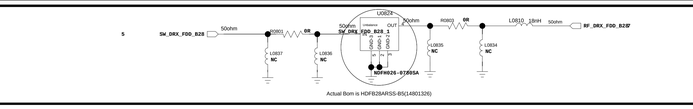

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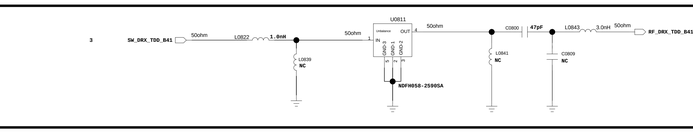

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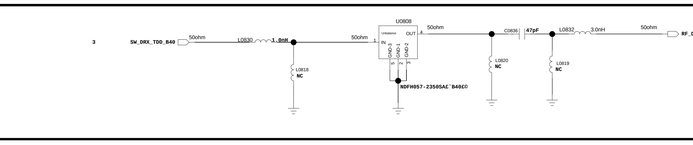
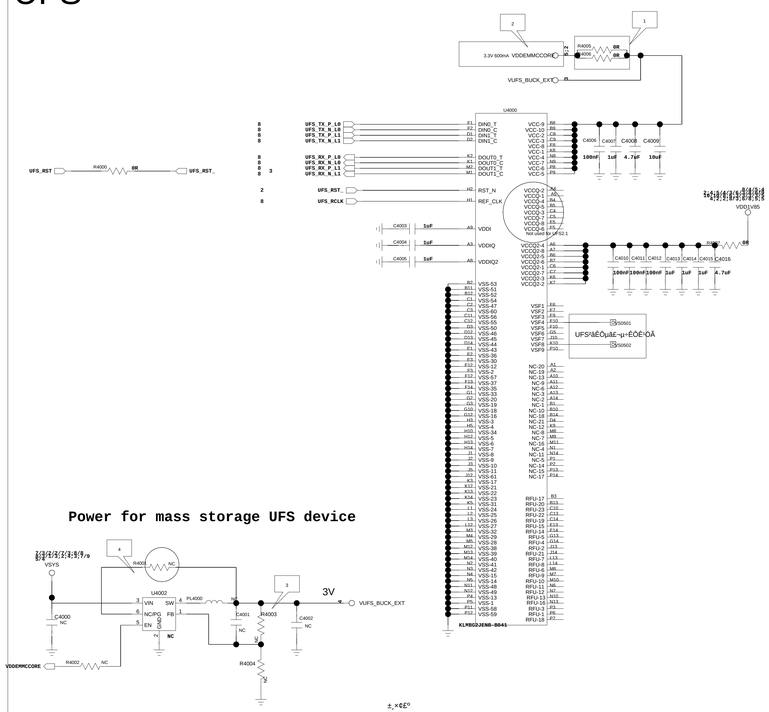


COMPANY: TRANSSION HOLDINGS				MODEL: J6563_F069_MAIN		Modified Date:	07/07/2023:16:40
DRAWN		DATED		TITLE:		VERSION:	SHEET: OF 23
CHECKED		DATED	< >	Confidentiality	CONFIDENTIAL		

REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:

UFS



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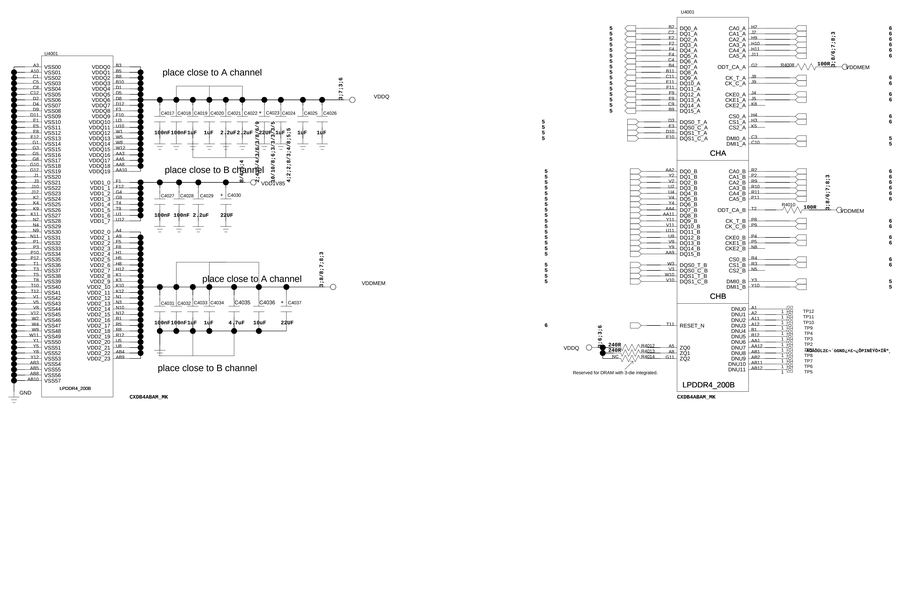
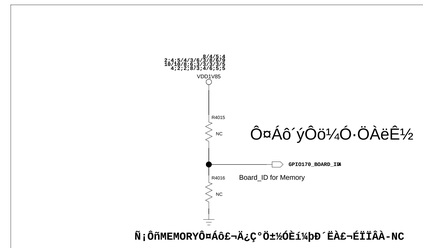
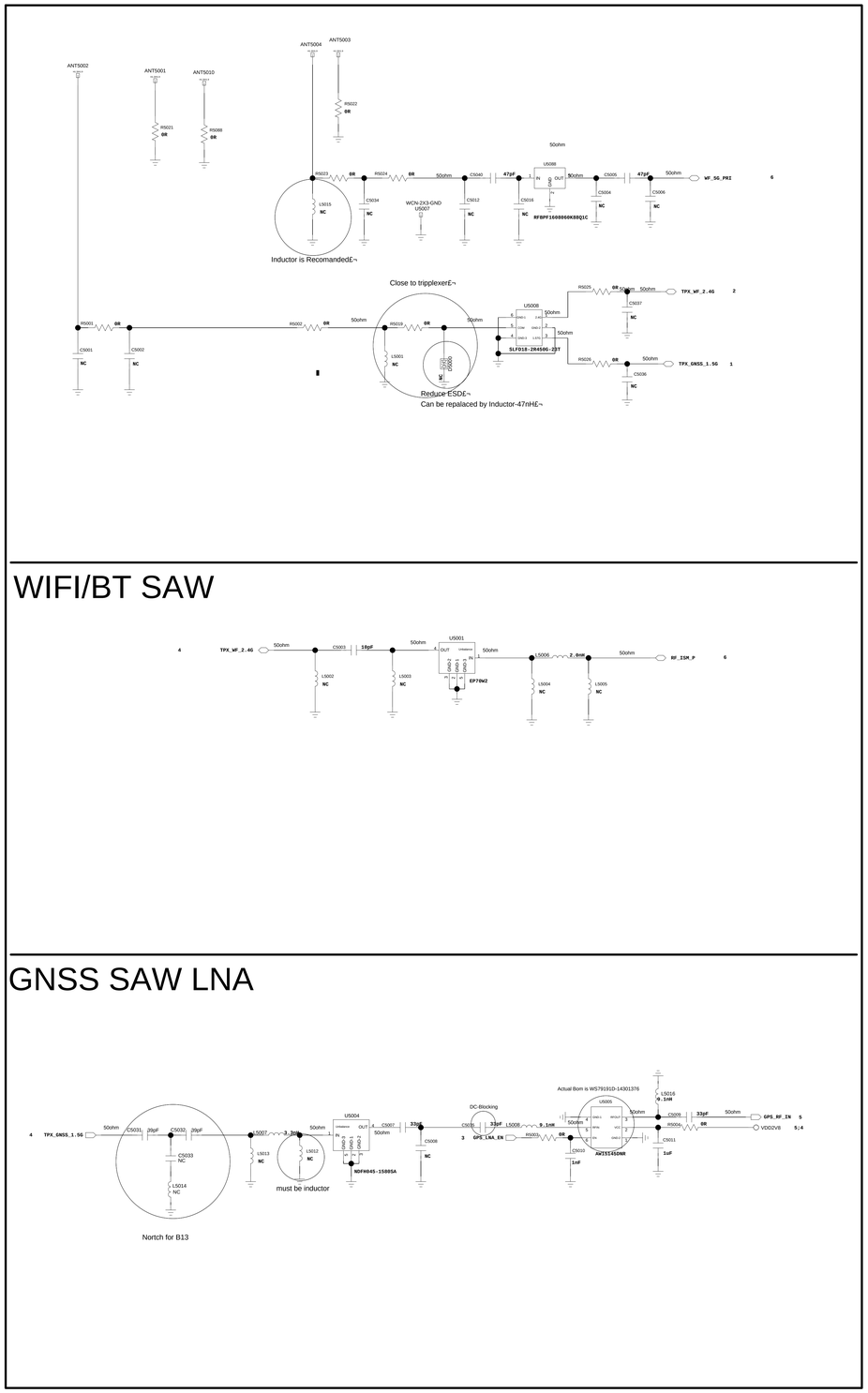




COMPANY: TRANSSION HOLDINGS			MODEL: J6563_F069_MAIN	Modified Date:	07/07/2023:16:40
DRAWN		DATED	TITLE:	VERSION:	SHEET: 0 OF 23
CHECKED		DATED < >	Confidentiality CONFIDENTIAL		

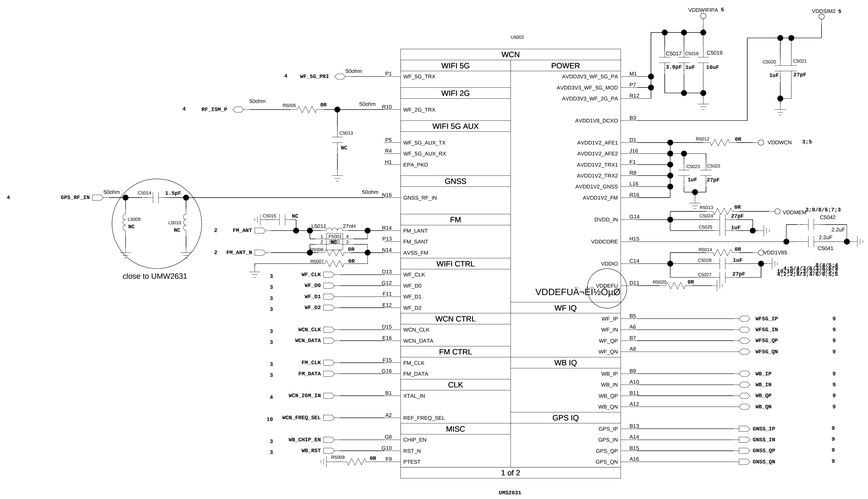
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

WIFI&BT ANT

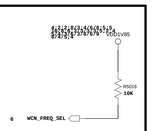


WIFI/BT SAW

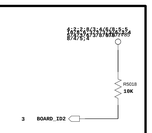
GNSS SAW LNA



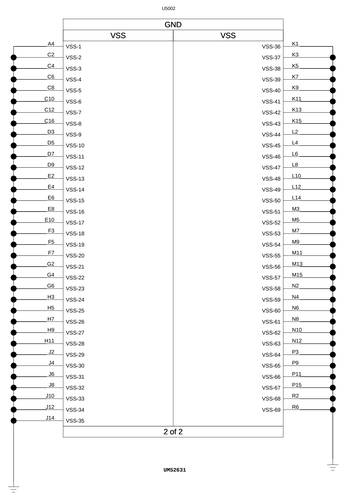
Bonding Option



FREQ_SEL---Internal Pull-up	
0	52MHz
1	26MHz



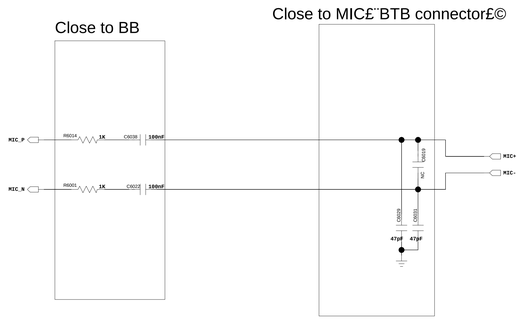
BOARD_ID2	WCN CLOCK
0	TCXO
1	TSX



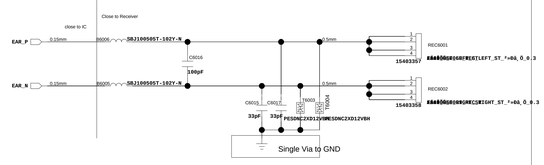
COMPANY: TRANSSION HOLDINGS			MODEL: J6563_F069_MAIN	Modified Date:	07/07/2023:16:40
DRAWN		DATED	TITLE:	VERSION:	SHEET: 23 OF 23
CHECKED		DATED < >	Confidentiality CONFIDENTIAL		

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

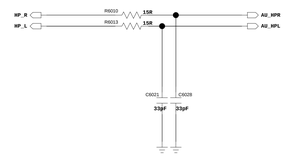
MAIN MIC



RECEIVER



3.5mm Ear Jack



Ear Jack suggest support:

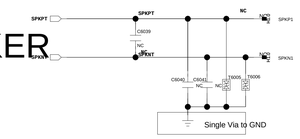
Earphone	Pin 4 & Pin 5
Plug in	Connect
Pull out	Disconnect

L,R,GND,MIC **CTIA**

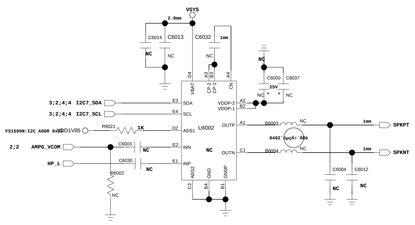
C3153 B3105
Close to Ear Jack's Pin 2(GAD)

T3125 is reserved for better ESD defense of FM_ANT IO

UP SPEAKER



UP SPEAKER



DWN SPEAKER

in SUB BOARD

HEAD MIC

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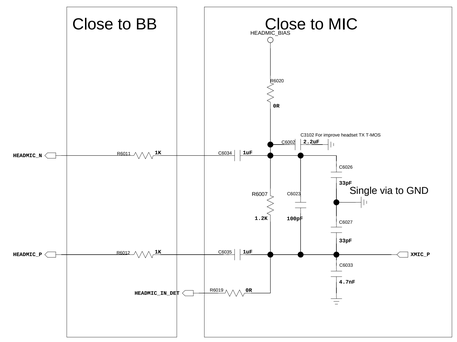

COMPANY: TRANSSION HOLDINGS			MODEL: J6563_F069_MAIN	Modified Date:	04/08/2023:18:19
DRAWN		DATED	TITLE:	VERSION:	SHEET: OF 23
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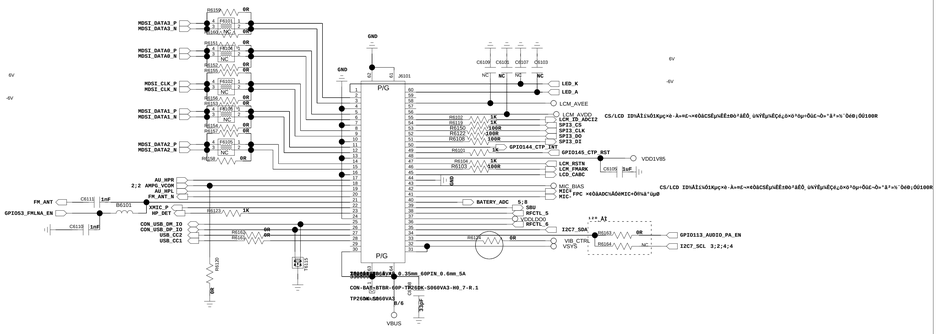
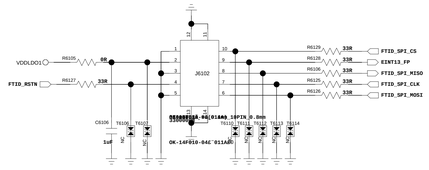
LCM



FingerPrint



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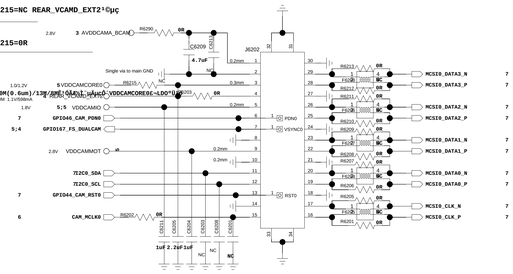
PERI_CAMERA_I

REAR CAM I (50M/13M/8M)

64M/50M/48M	R6203=0R	**R6215=NC REAR_VCAMD_EXT2¹©µç**
13M/16M/8M	R6203=NC	**R6215=0R**

¸ÃLDO©-ÊÇ°óÉãS50M(0.7um)Ê±°óÉ¹óÃ,50M(0.6um)/13M/8MÉ¡óÃ±Ê¼µ¼ÃÇÖ·VDDCAMCORE0È¡LDO?µ½VDD



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	SENSOR	I2C Address	VCAMA	VCAMD	VCAMIO	AFVDD	SAP
50M	HI-846(Q) AAA8322FA4	Write:0x40 Read:0x41	2.8V @78mA	1.1V @598mA	1.8V@ 31mA	2.8V@ 103mA	17201826(0.7um)
	SSK2N2QD-F1F4	Write:0x20 Read:0x21	2.8V @87.7mA	1.05V @260.7mA	1.8V@ 4mA	2.8V@ 100mA	17201711(0.64um)
32M	HI-1336-AA1338NXX-C2SAHX	Write:0x40 Read:0x41	2.8V @44mA	1.1V @106mA	1.8V@ 6mA	2.8V@ 125mA	17201835
	SSK3L8XX03-F1X	Write:0x20Read:0x21	2.8V @45mA	1.05V @160mA	1.8V@ 1.1mA	2.8V@ 125mA	17201836
8M	HM8N8AA8N8BNXX-B2CMCX	Write:0x40 Read:0x41	2.8V @62mA	1.2V @67mA	1.8V@ 2mA	2.8V@ 123mA	17201833
	GC8A3-9A11XA	Write:0x42Read:0x43	2.8V @80mA	1.2V @155mA	1.8V@ 10mA	2.8V@ 125mA	17201834

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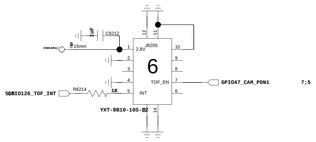
50M+Q

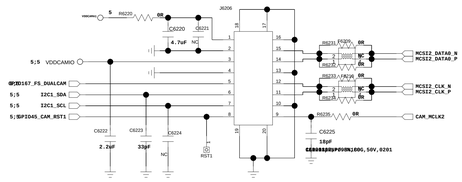
			VCAMA	PINS			SAP
¹âÃô			2.8	1.7~0.2			17201729
			2.8				17201744
			2.8				17201845

R6217/R6218 CO-PAD

R6214/R6216 CO-PAD



2M



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COMPANY: TRANSSION HOLDINGS				MODEL: **J6563_F069_MAIN**		Modified Date:	**07/07/2023:16:40**
DRAWN		DATED		TITLE:		VERSION:	SHEET: 0f **23**
CHECKED		DATED	< >	Confidentiality	CONFIDENTIAL		

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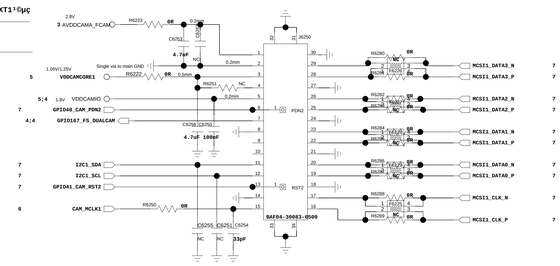
LTR	ECO NO:	APPROVED:	DATE:

Front Camera (32M/16M/8M/5M)

32M	R6251=0R	**R6222=NC VCAMD_EXT1¹ßµç**
5M/16M/8M	R6251=NC	R6222=0R



	SENSOR	I2C Address	VCAMA	VCAMD	VCAMIO	SAP
32M	GC32E1-W41XA	Write 0x54 Read 0x55	2.8V 43mA	1.1V 153mA	1.8V 4mA	
16M	HI-1631Q	Write 0x40 Read 0x41	2.8V 49mA	1.1V 115mA	1.8V 3mA	17331821
8M	GC8034-MC142	Write 0x6E Read 0x6F	2.8V 35mA	1.25V 140mA	1.8V 10mA	17331817
	GC5663-MEOD	Write 0x20 Read 0x21	2.8V 60mA	1.2V 150mA	1.8V 10mA	17331819
	OV08856-GA4A-2	Write 0x6C Read 0x6D	2.8V 28mA	1.2V 95mA	1.8V 2.7mA	17331820
5M	GC5035-MCHDO	0X6E(W)/0X6F(R)	2.8V 35mA	1.2V 80mA	1.8V 3mA	
	HI-556DDYNW1	0X20(W)/0X21(R)	2.8V 43mA	1.2V 48mA	1.8V 1mA	

COMPANY: TRANSSION HOLDINGS				MODEL:**J6563_F069_MAIN**		Modified Date:	**07/07/2023:16:40**
DRAWN		DATED		TITLE:		VERSION:	SHEET: OF**23**
CHECKED		DATED	< >	Confidentiality	CONFIDENTIAL		

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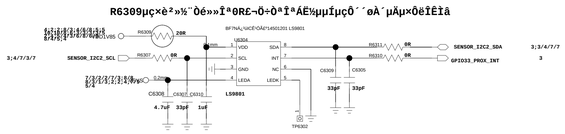
LTR	ECO NO:	APPROVED:	DATE:

ALS & PROX

STK3356210C address: Write 0x8C; Read 0x8D

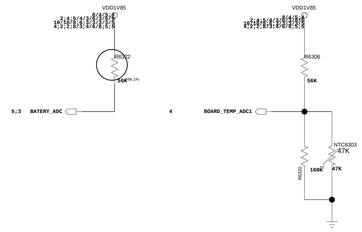


G-Sensor + Gyro Sensor

NTC

Thermistor to sense BATTERY_IC

Thermistor to sense BORAD_TEMP

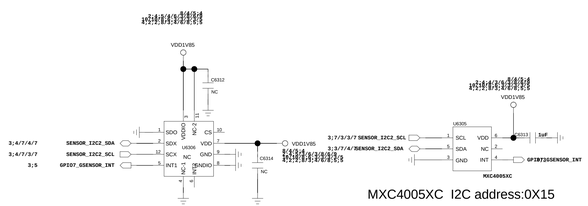


Thermistor to sense BORAD_TEMP
board sensor£¨black side £©
BB IC---->>>>>£¨3----4mm £©
PMIC---->>>>>£¨3----4mm £©

G Sensor

DA218-B I2C address:0x4e/0x4f

STK8BA50-S I2C address:0X18

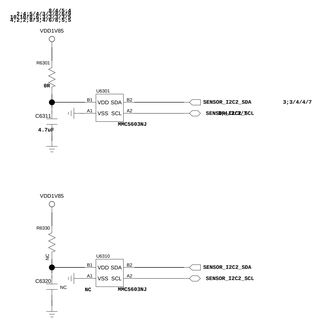


MXC4005XC I2C address:0X15

MC3413-P/MC3416 I2C address:0X98/0X99
SC7A20 I2C address:0X36/0X37

M-Sensor(COMPASS)

MMC5603:I2C ADDRESS:0x600Write/0x601Read)
QMC6308:I2C ADDRESS:0x30Write/0x35Read)



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DRAWN		DATED		TITLE:		VERSION:	SHEET: OF 23
CHECKED		DATED	< >	Confidentiality	CONFIDENTIAL		

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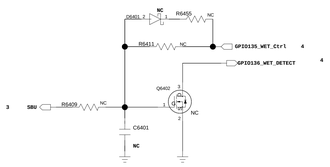
LTR	ECO NO:	APPROVED:	DATE:

Exchange IO

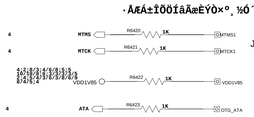
WET detect



Test Points

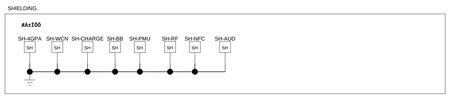
Bus/Vbat/GND1/BAT_ON/PWR/D+/D-/TX1/RX1/TX0/RX0·ÃÖÃBÃæ,ÇÒ¿ÉÊÓ

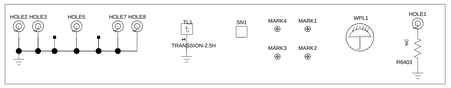
·ÃæÁ±ÎÕÖÍâÃæÈÝÒ×°¸½Ó´¦



JTAG_MEMORY

Put them on the main trace





FORCE DOWNLOAD

Strapping pin

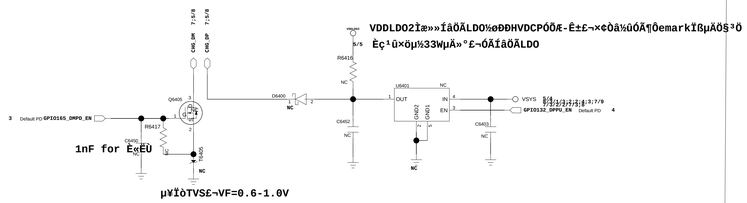
Ball name	Signal name in boot code	Value	Descriptions	Default vale
U1TXD	USB_DLOAD_EN	0	USB download(no time out)	1
		1	UART download	
ADI_SCLK	ARM_BOOT_MD2	0	SD boot(no time out)	1
		1	eMMC boot	

HVDCP TA

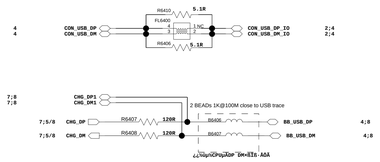
SGM41516D ×öÖ÷charger È«²¿NC

SD7693 ×öÖ÷charger È«²¿NC

ETA6937 ×öÖ÷charger È«²¿SMT



RFC



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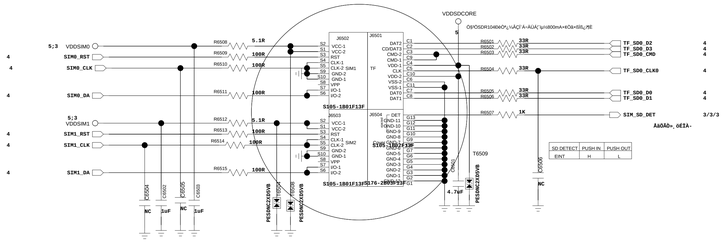
18W IC	T6405	R6407	R6408	ËµÃ÷
ETA6937	TVS-->PESDNC2XD5VU	50R	20R	**T6405 SMT ¶þ¼«¹Ü**
OTHERS	RES-->0R	120R	120R	**T6405 SMT 0R**

COMPANY: TRANSSION HOLDINGS			MODEL: J6563_F069_MAIN	Modified Date:	07/07/2023:18:50
DRAWN		DATED	TITLE:	VERSION:	SHEET: 23 OF 23
CHECKED		DATED < >	Confidentiality CONFIDENTIAL		

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

SIM0 & SIM1 & TF



SD DETECT	PUSH IN	PUSH OUT
ERY	H	L

Volume Key

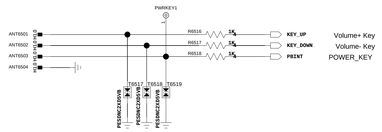
POWER KEY

Press KEYIN0+KEYOUT0/GND when power up will make the phone boot from USB

KEY Volume

Spport two keys 7S reset ,also support single key(PBINT) 7S reset

Two keys Reset :EXTRSTN+PBINT



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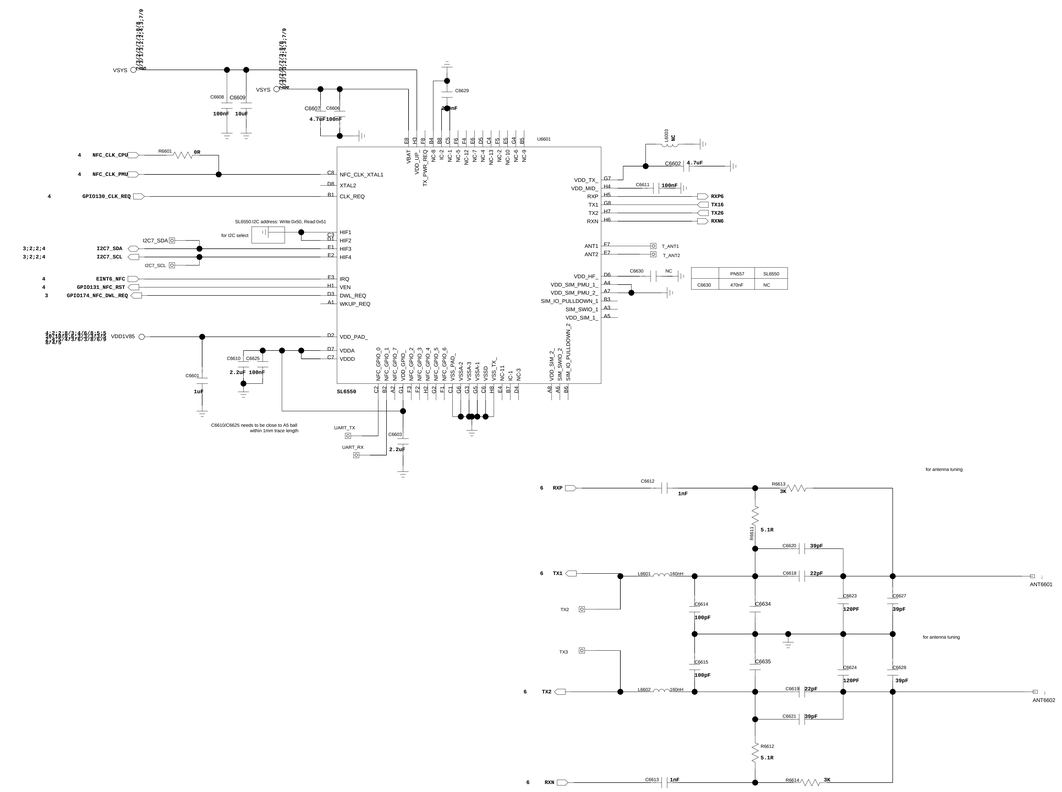
NFC



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DRAWN		DATED		TITLE:	VERSION:	SHEET: OF 23
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